

# Facilitating AADL Model Processing and Analysis with OSATE-DIM

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# **OSATE** Instance Model

Instance Model

#### blue: subcomponent reference red: classifier reference



Declarative Model



#### RAMSES Workflow (Refinement of AADL Models for the Synthesis of Embedded Systems)





#### View-Update Problem



🛞 IP PARIS

#### Solution: Delta-based Lens

![](_page_4_Figure_1.jpeg)

![](_page_4_Figure_2.jpeg)

(GetPut)  $s = put(\Phi,s)(s)$ (PutGet) get(put(u,s)(s)) = v'(PutPut) put(u',put(u,s)(s)) =(b)  $put(u \circ u',s)$ 

- s : Model State
  f : View-Generating Function
  v : View-State
  u : View-Update
  T : Translation
- => Declarative Model
- => Instantiation
- => Instance Model
- => Updates of Instance Model
- => Deinstantiation

f is not a bijection!

blue: subcomponent reference red: classifier reference

![](_page_5_Figure_2.jpeg)

Q. Add a property to *the\_sender* 

![](_page_5_Picture_4.jpeg)

AADL Property Value Determination

![](_page_6_Figure_2.jpeg)

\*from OSATE2 Help Contents > AADL2.0 reference manual

![](_page_6_Picture_4.jpeg)

blue: subcomponent reference red: classifier reference

![](_page_7_Figure_2.jpeg)

Q. Add a property to *the\_sender* Soln. Add property to *the\_sender\** or *sender.impl* or *sender* ??

![](_page_7_Picture_4.jpeg)

blue: subcomponent reference red: classifier reference

![](_page_8_Figure_2.jpeg)

Q. Add a property to *the\_sender* Soln. Add property to *the\_sender\** or *sender.impl* or *sender* or to *proc.impl* (*applies to the\_sender\**) ??

![](_page_8_Picture_4.jpeg)

#### Too many choices and decisions for de-instantiation in AADL-OSATE!

![](_page_9_Picture_1.jpeg)

# Another Example: Need for automated de-instantiation

```
system main
end main;
system implementation main.impl
    subcomponents
        proc1: process proc.impl;
        proc2: process proc.impl;
end main.impl;
process proc
end proc;
process implementation proc.impl
    subcomponents
        the sender: thread sender;
        the receiver: thread receiver;
    connections
        cnx: feature the sender.p -> the receiver.p;
end proc.impl;
```

```
thread sender
```

package demo models

public

features
 p: out feature;
end sender;

```
thread receiver
    features
    p: in feature;
end receiver;
end demo_models;
```

![](_page_10_Figure_5.jpeg)

![](_page_10_Figure_6.jpeg)

proc1 and proc2 have the same classifier proc.impl

![](_page_10_Picture_8.jpeg)

## Another Example: Need for automated de-instantiation

![](_page_11_Figure_1.jpeg)

We want to change the abstract feature *p* in *proc1.the\_sender* to data port

![](_page_11_Picture_3.jpeg)

## Example: Need for automated de-instantiation

![](_page_12_Figure_1.jpeg)

Simple! Change the corresponding feature.

![](_page_12_Picture_3.jpeg)

## Example: Need for automated de-instantiation

![](_page_13_Figure_1.jpeg)

But this also changes *p* in *proc2.the\_sender* !!

![](_page_13_Picture_3.jpeg)

![](_page_14_Figure_0.jpeg)

main

Solution with preservation of information is very complicated with many extensions and refinements, even for a simple instance update!

main.impl

proc2\*

proc1\*
roc.impl\_ext

proc.impl

proc2

the sender

the receiver

package demo\_models\_refined
public
 system main
 end main;

system implementation main.impl
 subcomponents
 proc1: process proc.impl\_ext;
 proc2: process proc.impl;

end main.impl;

process proc
end proc;

process implementation proc.impl subcomponents the\_sender: thread sender; the\_receiver: thread receiver; connections cnx: feature the\_sender.p -> the\_receiver.p; end proc.impl; process implementation proc.impl\_ext extends proc.impl subcomponents the\_sender: refined to thread sender.ext;

end proc.impl\_ext;

thread sender features p: out feature; end sender;

thread sender\_ext extends sender
 features
 p: refined to out data port;
end sender\_ext;

thread receiver
 features
 p: in feature;
end receiver;

end demo\_models\_refined;

![](_page_14_Picture_12.jpeg)

sender ext

sender

#### Real-life Scenario: RAMSES

![](_page_15_Figure_1.jpeg)

Simplest refinement pattern in RAMSES:

Changing data port connection between two threads by replacing with a shared data component.

The data port features are changed to data access features.

![](_page_15_Picture_5.jpeg)

Too many choices for de-instantiation in AADL! + Complications for information preservation due to many dependencies between elements and modularity.

 $\equiv$ 

Makes de-instantiation of updates highly complex; requiring automation

![](_page_16_Picture_3.jpeg)

#### **OSATE-DIM**

- **OSATE** Declarative-Instance Mapping
- Eclipse/OSATE-based plugin
- Graph Transformations
  - VIATRA Ο
    - **Graphical Queries**
    - Model Transformation Rules
    - Reactive nature (incremental)

![](_page_17_Figure_8.jpeg)

![](_page_17_Picture_9.jpeg)

#### **OSATE-DIM** Values/Aims

- Maximum Information Preservation
- Least/Minimal Change
- Very-well behaved lens (3 Lens laws)
  - $\circ$  No extraneous model updates.
  - Equality of updated-model state with updated view-state.
  - Composability of updates.
- Flexibility
  - Scenarios
  - User preferences

![](_page_18_Picture_10.jpeg)

# **Transformation Scenarios**

![](_page_19_Figure_1.jpeg)

![](_page_19_Picture_2.jpeg)

#### Case Study: MC-DAG

![](_page_20_Figure_1.jpeg)

- Addition of *Property Associations* (RAMSES::Execution\_Slots) for each *Thread*.
- Contain static scheduling tables for each *Thread* in different *Modes* LO and HI.
- Properties also reference the core and memory binding model elements, not just static data.

![](_page_20_Picture_5.jpeg)

# Case Study: RAMSES

![](_page_21_Figure_1.jpeg)

- Addition of 2 (+40) *Data Components* to a *Process Component*, which are shared by two threads.
- The *Port Features* interfacing the two threads with each other are changed to *Data Access* kinds.
- New *Data Access Connections* are also added between the shared *Data Components* and the *Threads*.
- The added *Data Components* have varying numbers of *Properties*, and the total number of newly added properties is 122.

![](_page_21_Picture_6.jpeg)

#### Conclusion

Introduced View-Update Problem in AADL-OSATE

OSATE-DIM is an automated solution for synchronizing Instance and Declarative models: 'de-instantiating' the Instance model

Three different scenarios (2 supported currently)

Wide range of view-updates supported

Tested on a preliminary test-bench

Simplifies the development of AADL model refinement tools

![](_page_22_Picture_7.jpeg)

Future Work

Complete Implementation of Delta Out-of-place scenario

Further validation

Integration with RAMSES

Concepts have potential to be used for "transpilation"

![](_page_23_Picture_5.jpeg)

#### Publications

Rakshit Mittal, Dominique Blouin, Anish Bhobe, Soumyadip Bandyopadhyay. 2022. Solving the instance modelview update problem in AADL. In Proceedings of the 25th International Conference on Model Driven Engineering Languages and Systems (MODELS '22). Association for Computing Machinery, New York, NY, USA, 55–65. https://doi.org/10.1145/3550355.3552396

Rakshit Mittal, Dominique Blouin. 2022. OSATE-DIM solves the instance model-view update problem in AADL. In Proceedings of the 25th International Conference on Model Driven Engineering Languages and Systems: Companion Proceedings (MODELS '22). Association for Computing Machinery, New York, NY, USA, 1–6. https://doi.org/10.1145/3550356.3559083

Rakshit Mittal. 2022. The instance model-view update problem in AADL. In Proceedings of the 25th International Conference on Model Driven Engineering Languages and Systems: Companion Proceedings (MODELS '22). Association for Computing Machinery, New York, NY, USA, 221–224. https://doi.org/10.1145/3550356.3552373

![](_page_24_Picture_4.jpeg)

Thank you for your attention Questions? with Dominique

![](_page_25_Picture_1.jpeg)

Tool Webpage: <u>mem4csd.telecom-paristech.fr</u> Zenodo artifact DOI: 10.5281/zenodo.6971720

![](_page_25_Picture_3.jpeg)

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![](_page_25_Picture_5.jpeg)